

In re the Application of:

KOMIYAMA et al.

Serial No. 09-776,528

Filed: February 4, 2001

For: METHODS FOR MANUFACTURING  
SEMICONDUCTOR CHIPS, METHODS FOR  
MANUFACTURING SEMICONDUCTOR  
DEVICES, SEMICONDUCTOR CHIPS,  
SEMICONDUCTOR DEVICES, CONNECTION  
SUBSTRATES AND ELECTRONIC DEVICES

Group Art Unit: 2814

Examiner: Ha, Nathan W.

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.

A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.

It is believe that no additional fee is required.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO PREVIOUSLY PAID FOR		PRESENT EXTRA RATE		ADDIT. FEE	OR	RATE		ADDIT. FEE
TOTAL	25	MINUS	23	=	2	x	\$9	OR	x 18		\$36
INDEP CLAIMS *	7	MINUS	7	=	0	x	\$42	OR	x 84		\$0
FIRST PRESENTATION OF MULTIPLE DEP. CLAIM						+	\$140	OR	+ 280		\$0
					TOTAL		\$0	OR	TOTAL		\$36

Please charge Deposit Account No. 50-0585 the amount of \$\_\_\_ to cover the extension fee and also the amount of \$\_\_\_ to cover the claim fee. A duplicate copy of this sheet is enclosed.

☒ A check in the amount of \$ 110 to cover the extension fee is enclosed.

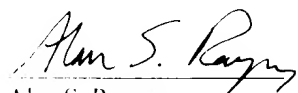
☒ A check in the amount of \$ 36 to cover the filing fee for additional claims is enclosed.

☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. No. 50-0585. A duplicate of this sheet is enclosed.

☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,



Alan S. Raynes

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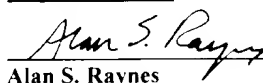
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Dated: Aug. 25, 2003

**Certificate of Mailing**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 25, 2003.



Alan S. Raynes

Aug. 25, 2003  
Date



15.32/5685

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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For: METHODS FOR MANUFACTURING	)	
SEMICONDUCTOR CHIPS, METHODS FOR	)	
MANUFACTURING SEMICONDUCTOR	)	
DEVICES, SEMICONDUCTOR CHIPS,	)	<b><u>AMENDMENT</u></b>
SEMICONDUCTOR DEVICES, CONNECTION	)	
SUBSTRATES AND ELECTRONIC DEVICES	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Dear Sirs:

In response to the Office Action dated April 24, 2003, the response due by August 25, 2003 (Aug. 24 was a Sunday) by the enclosed petition for extension of time, please enter and consider the following.